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THE UNITED STATES PATENT AND TRADEMARK OFFICE

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

APPLICANT: Thomas R. Keyser

CASE NO.: H0004213-5866US  
(P04,0000)

SERIAL NO.: 10/811,767

CONFIRMATION NO.: unknown

FILING DATE: March 29, 2004

GROUP ART UNIT: unknown

INVENTION: "SILICON OPTOELECTRONIC DEVICE"

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SIR:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR § § § 1.56, 1.97, and 1.98. As of the date of mailing this Information Disclosure Statement, a first Office Action on the merits has not been received in connection with this application, and therefore, this Information Disclosure Statement is in compliance with 37 C.F.R. §1.97(b)(3), and no fee is necessary. The undersigned counsel certifies, moreover, that each item contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement, as required by 37 C.F.R. §1.97(e). In the event that this Information Disclosure Statement crosses in the mail with a first Office Action, therefore, this Information Disclosure Statement satisfies 37 C.F.R. (c)(1), and still no fee is necessary. Enclosed is a copy of the International Search Report. Also enclosed is a further copy of International Search Report for related U.S. application 10/755,212.

The Commissioner is hereby authorized to charge any additional fees that may be required, or to credit any overpayment, to account No. 501519. A duplicate of this sheet is enclosed.

An early and favorable action on the merits is respectfully requested.

Respectfully submitted,



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**CERTIFICATE OF MAILING**

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PTO/SB/08a (08-03)

Approved for use through 7/31/2006. OMB 0651-0031

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet

1

of

2

**Complete if Known**

Application Number

10/811,767

Filing Date

March 29, 2004

First Named Inventor

Thomas R. Keyser

Art Unit

unknown

Examiner Name

unknown

Attorney Docket Number

H0004213-5866US

**U.S. PATENT DOCUMENTS**

Examiner Initials	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	AA	US 2002/0164143 A1	11-07-2002	Csutak et al.	
	AB	US 2004/0207016 A1	10-21-2004	Patel et al.	
	AC	US 2002/0172464 A1	11-21-2002	Delwala	
	AD	US 2003/0098289 A1	05-29-2003	Zheng et al.	
	AE	US 2003/0059190 A1	03-27-2003	Gunn, III et al.	
	AF	6,150,266	11-21-2000	Lin et al.	
	AG				
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
	AU	WO 2004/088394 A2	10-14-2004			
	AV	WO 2004/088396 A2	10-14-2004			
	AW	WO 03/023468 A1	03-20-2003			
	AX					

Examiner  
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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	<b>10/811,767</b>
				Filing Date	<b>March 29, 2004</b>
				First Named Inventor	<b>Thomas R. Keyser</b>
				Art Unit	<b>Unknown</b>
				Examiner Name	<b>Unknown</b>
Sheet	<b>2</b>	Of	<b>2</b>	Attorney Docket Number	<b>H0004213-5866US (P04,0000)</b>

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
	AY	Hilleringmann et al., "Optoelectronic System Integration on Silicon: Waveguides, Photodetectors, and VLSI CMOS Circuits on One Chip", IEEE Transactions on Electron Devices 42 (1995) Mai, No. 5, New York, NY, US, pgs. 841-846.	
	AZ	Liu et al., "Fast silicon optical modulator", Proc. Of the SPIE - Optoelectronic Integration on Silicon - Jul. 2004, Vol. 5357, pgs. 35-44.	
	AAA	Liu et al., "A high-speed silicon optical modulator based on a metal-oxide-semiconductor capacitor", February 12, 2004, Nature Publishing Group, Vol. 427, pgs. 615-618.	
	ABB		
	ACC		
	ADD		
	AEE		
	AFF		
	AGG		
	AHH		
	AII		
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	ALL		
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<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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